

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#6 Election

11/7/02
VB

In re Patent Application of:

| | | | |
|-------------------|--|------------------------|------------------|
| Name: | Zhizhang Chen, et al | Date: | October 29, 2002 |
| Serial No. | 09/817,703 | Examiner: | Foong, Suk San |
| Filed: | March 26 2001 | Group Art Unit: | 2833 |
| PD No. | 10002861-1 | Conf. No.: | 9286 |
| For: | LDMOS And CMOS Integrated Circuit And Method of Making | | |

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Assistant Commissioner for Patents
Washington, DC 20231

RESPONSE TO RESTRICTION REQUIREMENT

Sir:

In response to the Election Requirement mailed October 01, 2002, Applicant elects Group I, Claims 10, 12, 14, and 16, without traverse.

Respectfully submitted,

Hewlett-Packard Company
1000 NE Circle Blvd., m/s 422B
Corvallis, OR 97330
(541) 715-4197

Zhizhang Chen, et al.

By:

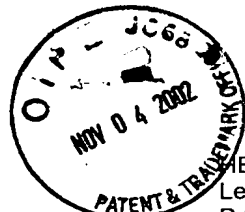


Timothy F. Myers
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Reg. No. 42,919

I hereby certify that this correspondence is being deposited on **October 29, 2002**, with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington D.C. 20231.


Timothy F. Myers

2833



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Attorney Docket No: 10002861-1

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ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON DC 20231

TRANSMITTAL LETTER

Sir:

Transmitted herewith is/are the following in the above-identified application:

- () Response After Final Under 37 CFR §1.116
- () Supplemental Declaration
- (X) Other: Response to Restriction Requirement

There is no charge for this correspondence; no new or additional matters are involved.

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Respectfully submitted,

Zhizhang Chen, et al.

By: Timothy F. Myers
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Timothy F. Myers
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